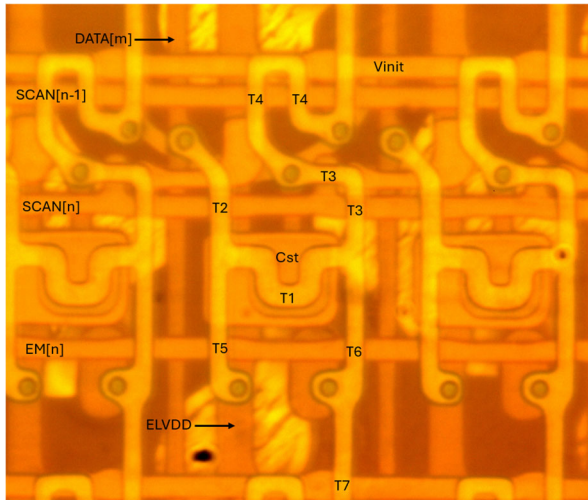
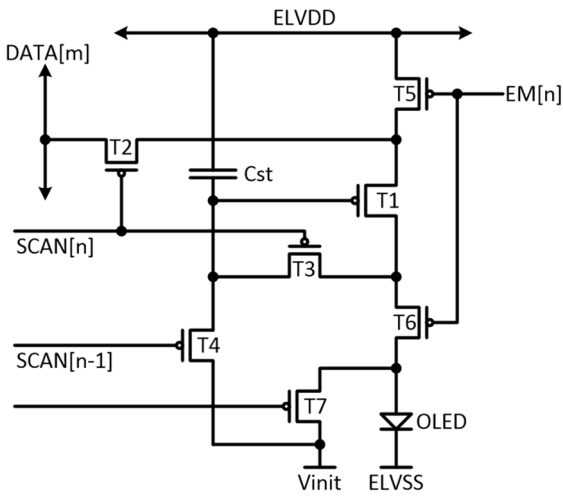
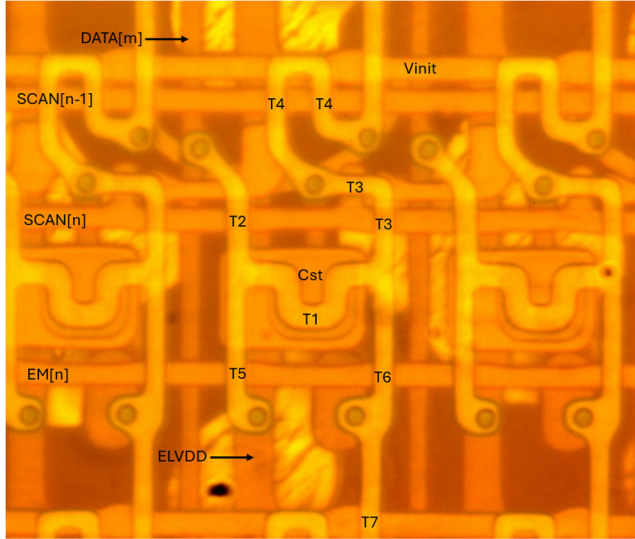
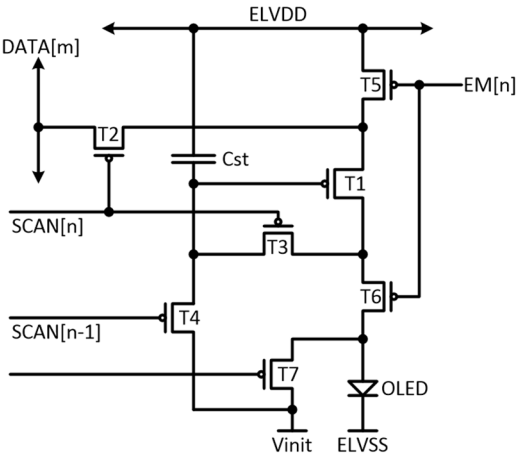
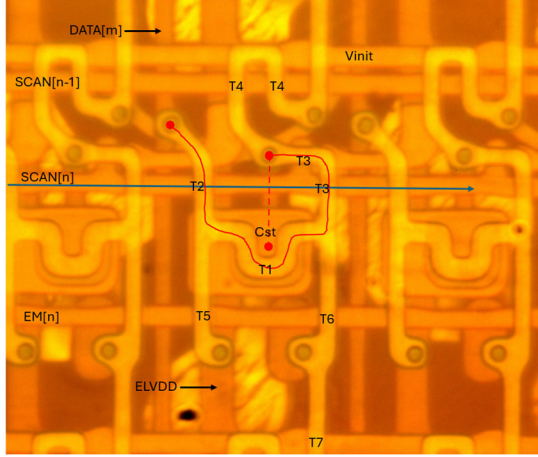
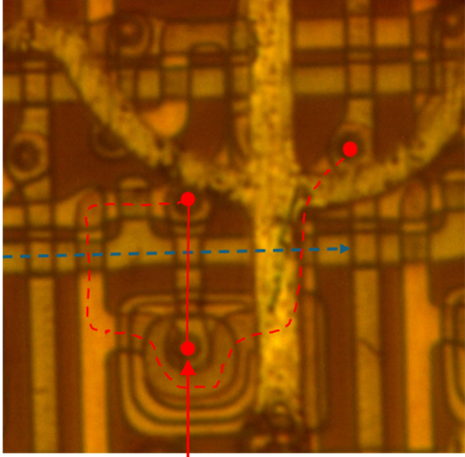


# EXHIBIT F

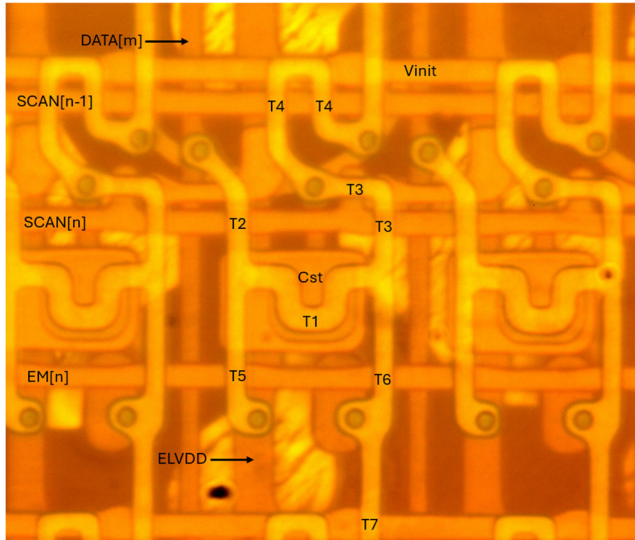
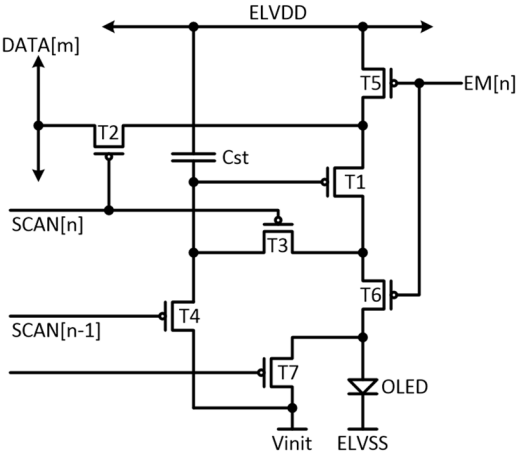
<b>Claim 15</b>	<b>Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)</b>
15[pre] A pixel circuit in an organic light emitting device, comprising:	<p>The UE-MBRPTL015 includes an organic light-emitting diode (“OLED”) display.</p> <div data-bbox="829 329 1717 1190"></div>

Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[pre] A pixel circuit in an organic light emitting device, comprising:</p> <p>(cont’d)</p>	<p>The UE-MBRPTL015 comprises a pixel circuit in an organic light-emitting device. The annotated backside image (<i>below left</i>) shows a pixel circuit of the Ultimate Eshop MBRPTL015, including transistors (T1–T7) and a capacitor (Cst). An exemplary circuit diagram of the pixel circuit of the Ultimate Eshop MBRPTL015 is also shown (<i>below right</i>).</p> <div style="display: flex; justify-content: space-around; align-items: center;">   </div>

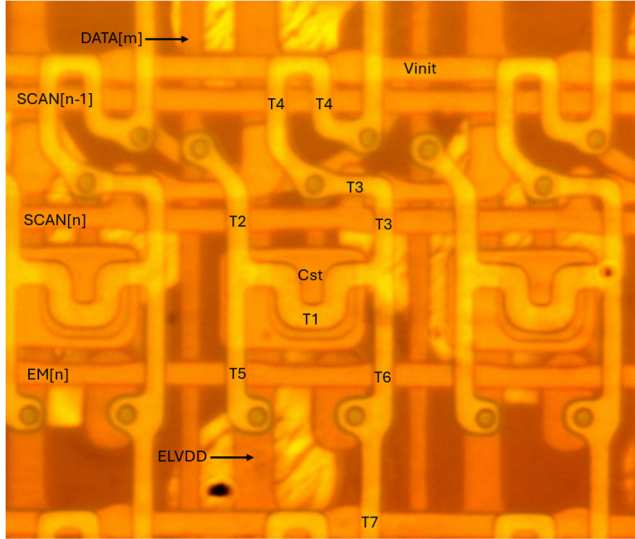
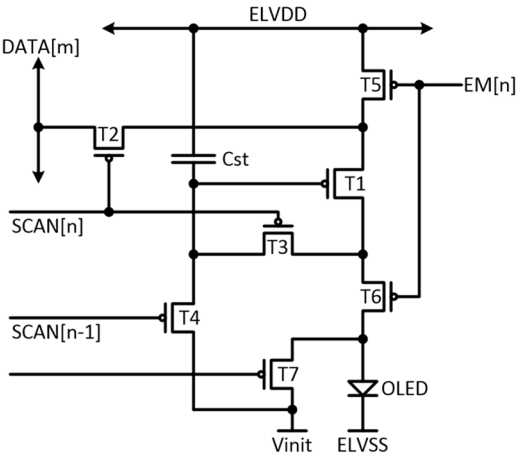
Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[a] a first transistor including a gate to which a current scan signal is applied, and a source to which a data signal voltage is applied;</p>	<p>The pixel circuit of the UE-MBRPTL015 has a first transistor including a gate to which a current scan signal is applied, and a source to which a data signal voltage is applied. As shown in the annotated backside image below, a source of the first transistor <b>T2</b> is coupled to a data line <b>DATA[m]</b> for delivering a data signal voltage. The gate of first transistor <b>T2</b> is coupled to a scan line <b>SCAN[n]</b> that provides a current scan line signal. In response to an active (low) current scan line signal delivered on scan line <b>SCAN[n]</b>, first transistor <b>T2</b> delivers a data signal voltage from data line <b>DATA[m]</b>.</p> <div style="display: flex; justify-content: space-around; align-items: flex-start;">   </div>

Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[a] a first transistor including a gate to which a current scan signal is applied, and a source to which a data signal voltage is applied;</p> <p>(cont’d)</p>	<p>For example, as shown in the exemplary annotated images below, in response to an active (low) current scan line signal (blue line) delivered on scan line <b>SCAN[n]</b>, first transistor <b>T2</b> delivers the data signal voltage (red line) present on data line <b>DATA[m]</b>. The annotated images below depict an exemplary voltage path from data line <b>DATA[m]</b> through transistors <b>T2-T1-T3</b> to the gate of transistor <b>T1</b>.</p> <div style="display: flex; justify-content: space-around; align-items: flex-start;"> <div style="text-align: center;"> <p><b>Delivery of Data Signal Voltage (backside image)</b></p>  </div> <div style="text-align: center;"> <p><b>Delivery of Data Signal Voltage (front-side image)</b></p>  <p>Contact Hole (to T1 Gate Electrode)</p> </div> </div>

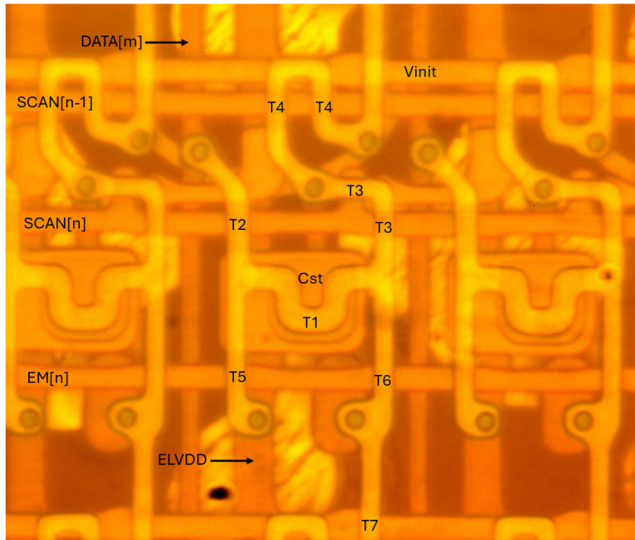
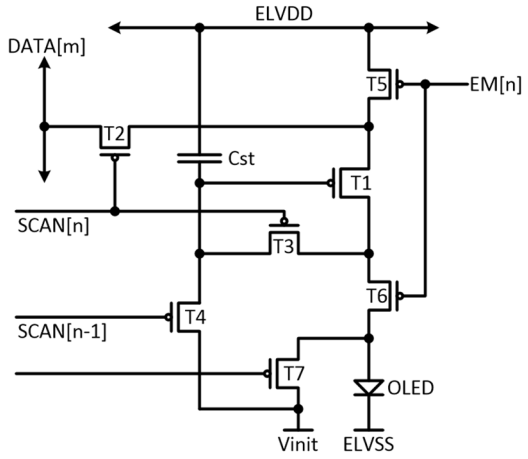
<p><b>Claim 15</b></p>	<p><b>Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)</b></p>
<p>15[b] a second transistor whose source is coupled to a drain of the first transistor;</p>	<p>The pixel circuit of the UE-MBRPTL015 has a second transistor whose source is coupled to a drain of the first transistor. As shown in the annotated backside image below, the source of the second transistor <b>T1</b> is coupled to the drain of first transistor <b>T2</b>.</p> <div data-bbox="695 470 1852 1006"> <p>The figure consists of two parts. On the left is an annotated backside image of the pixel circuit, showing various components labeled: DATA[m], SCAN[n-1], SCAN[n], EM[n], ELVDD, Vinit, T1, T2, T3, T4, T5, T6, and T7. On the right is a schematic diagram of the pixel circuit. The circuit includes a first transistor T2 with its gate connected to DATA[m] and its source to a node connected to T1. T1 has its gate connected to SCAN[n] and its source to the drain of T2. T3 has its gate connected to SCAN[n] and its source to the drain of T1. T4 has its gate connected to SCAN[n-1] and its source to the drain of T3. T5 has its gate connected to ELVDD and its source to the drain of T4. T6 has its gate connected to ELVDD and its source to the drain of T5. T7 has its gate connected to ELVDD and its source to the drain of T6. The circuit also includes a capacitor Cst connected between the gate of T2 and the node between T2 and T1. The output of the circuit is connected to an OLED pixel, which is connected to ELVSS. The circuit is powered by ELVDD and ELVSS, and has a Vinit input.</p> </div>

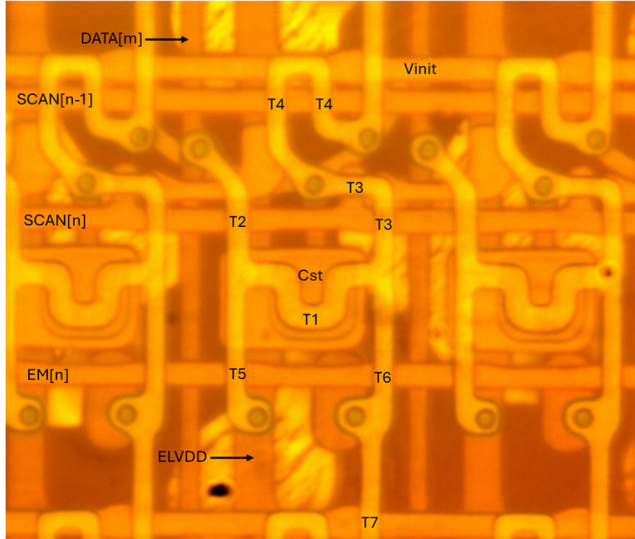
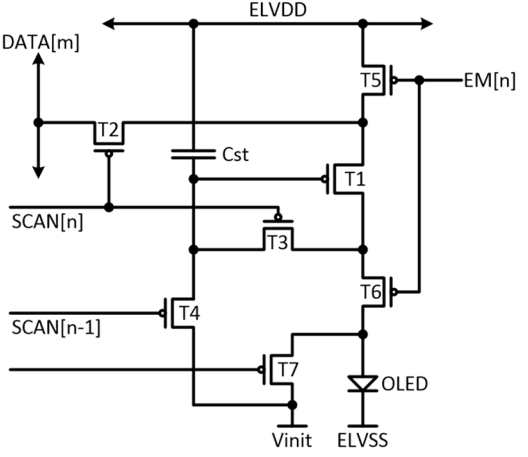
Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[c] a third transistor whose drain and source are connected between a gate and a drain of the second transistor;</p>	<p>The pixel circuit of the UE-MBRPTL015 has a third transistor whose drain and source are connected between a gate and a drain of the second transistor. As shown in the annotated backside image below, a third transistor <b>T3</b> has drain and source electrodes connected between gate and drain electrodes of the second transistor <b>T1</b>, respectively.</p> <div style="display: flex; align-items: center; justify-content: space-around;">   </div>

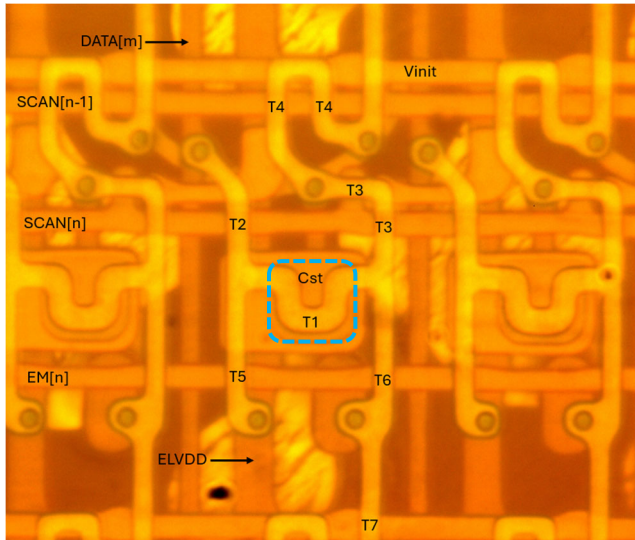
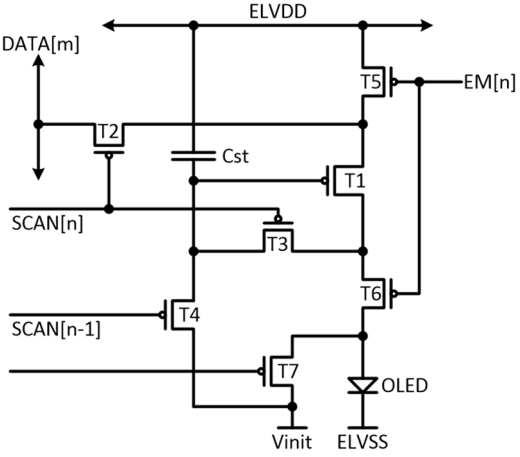


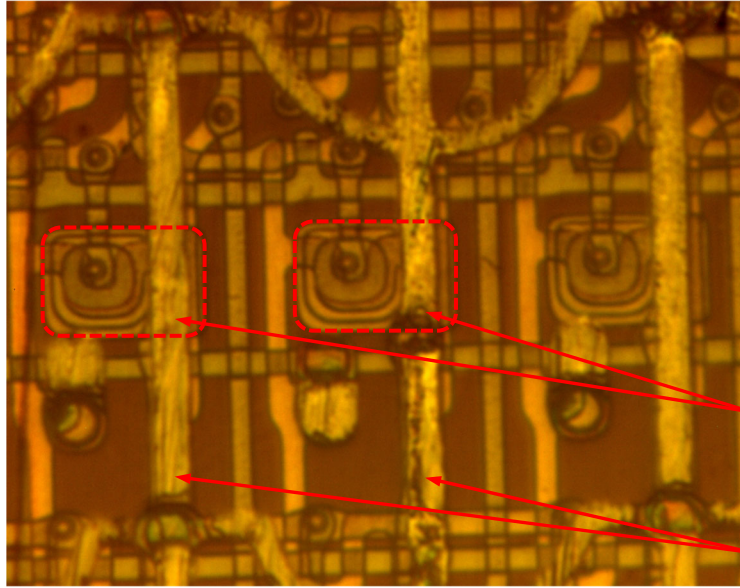
Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[d] a fourth transistor including a gate to which a current light-emitting signal is applied, a source to which a power supply voltage is applied, and a drain coupled to the source of the second transistor;</p>	<p>The pixel circuit of the UE-MBRPTL015 has a fourth transistor including a gate to which a current light-emitting signal is applied, a source to which a power supply voltage is applied, and a drain coupled to the source of the second transistor. In the annotated backside image below, a fourth transistor <b>T5</b> has a source coupled to <b>ELVDD</b> and a drain coupled to the source of transistor <b>T1</b>. In response to an active (low) current light-emitting signal delivered on emission line <b>EM[n]</b>, fourth transistor <b>T5</b> delivers a power supply voltage from <b>ELVDD</b> to second transistor <b>T1</b>.</p> <div style="display: flex; justify-content: space-around; align-items: center;">   </div>



Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[e] a fifth transistor including a gate to which the current light-emitting signal is applied, a source coupled to the drain of the second transistor, and a drain coupled to one terminal of an electroluminescent element;</p>	<p>The pixel circuit of the UE-MBRPTL015 has a fifth transistor including a gate to which the current light-emitting signal is applied, a source coupled to the drain of the second transistor, and a drain coupled to one terminal of an electroluminescent element. In the annotated backside image below, a fifth transistor <b>T6</b> has a source coupled to a drain of second transistor <b>T1</b>, and a drain is coupled to one terminal of an electroluminescent element (<b>OLED</b>) through a contact hole. As further shown in the annotated image below, in response to an active (low) current light-emitting signal delivered on emission line <b>EM[n]</b>, fifth transistor <b>T6</b> turns on and delivers driving current that flows through second transistor <b>T1</b> to the <b>OLED</b>.</p> <div style="display: flex; justify-content: space-around; align-items: center;">   </div>

Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[f] the electroluminescent element having the one terminal coupled to the drain of the fifth transistor and the other terminal grounded; and</p>	<p>The pixel circuit of the UE-MBRPTL015 has an electroluminescent element with one terminal coupled to the drain of the fifth transistor and the other terminal grounded.</p> <p>As shown in the annotated backside image below, the pixel circuit has an electroluminescent element (<b>OLED</b>) with a first terminal, the anode, coupled to the drain of the fifth transistor <b>T6</b>. The other terminal of the <b>OLED</b> is grounded to facilitate the flow of current from fifth transistor <b>T6</b> through the anode of the <b>OLED</b> to the cathode in the light-emission phase.</p> <div style="display: flex; justify-content: space-around; align-items: center;">   </div>

Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[g] a capacitor in which one terminal of the capacitor is coupled to the gate of the second transistor and a power supply voltage is applied to the other terminal of the capacitor.</p>	<p>The pixel circuit of the UE-MBRPTL015 has a capacitor in which one terminal of the capacitor is coupled to the gate of the second transistor and a power supply voltage is applied to the other terminal of the capacitor.</p> <p>As shown in the annotated backside image below, blue dashes outline the lower plate of the capacitor <b>Cst</b>, which is coupled to the gate of the second transistor <b>T1</b>.</p> <div style="display: flex; justify-content: space-around; align-items: center;">   </div>

Claim 15	Ultimate Eshop MBRPTL015 OLED Display (“UE-MBRPTL015”)
<p>15[g] a capacitor in which one terminal of the capacitor is coupled to the gate of the second transistor and a power supply voltage is applied to the other terminal of the capacitor.</p> <p>(cont’d)</p>	<p>As shown in the annotated front-side image below, red dashes outline the upper plate of the capacitor <b>Cst</b> that is coupled to the power supply voltage <b>ELVDD</b>.</p> <div data-bbox="905 435 1856 1019">  <p>The image is a microscopic view of the display's internal circuitry, showing a grid of vertical and horizontal lines. Two specific areas are highlighted with red dashed rectangles. Red arrows point from these areas to labels on the right. The label 'Contact Holes (to ELVDD)' points to the top of the dashed rectangles. The label 'Power Supply Voltage (ELVDD)' points to the bottom of the dashed rectangles.</p> </div>